

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:) Confirmation No.: 2677
Hisato Shinohara et al.) Examiner: Marianne L. Padgett
Serial No. 08/169,127) Group Art Unit: 1715
Filed: December 20, 1993)
For: LASER IRRADIATION METHOD)

NOTICE OF APPEAL TO THE FEDERAL CIRCUIT

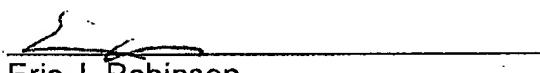
Mail Stop 8
Director of the U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 35 U.S.C. § 141 and 142 and 37 C.F.R. § 1.302 and 1.304, the Applicant hereby appeals to the Court of Appeals for the Federal Circuit from the Decision on Appeal of the Board of Patent Appeals and Interferences, dated July 29, 2010, and the Decision on Request for Rehearing rendered on November 10, 2010, including all rejected claims. Copies of both decisions are enclosed. This Notice of Appeal to the Federal Circuit is being filed within two months of the date of the Decision on Request for Rehearing, and is therefore believed to be timely filed.

The Commissioner is hereby authorized to charge any required fee associated with this Notice of Appeal to Deposit Account No. 50-2280.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285
Virginia State Bar No. 35,895

Robinson Intellectual Property Law Office, P.C.
3975 Fair Ridge Drive, Suite 20 North
Fairfax, Virginia 22033
(571) 434-6789

CERTIFICATE OF SERVICE

I, Sean C. Flood, do hereby certify that this Notice of Appeal to the Federal Circuit is being served by hand to the Office of Solicitor at the following address on January 10, 2011.

600 Dulany Street,
Madison West Building, Room 8C43,
Alexandria, VA 22314

Also, pursuant to MPEP § 1216.01, three copies of this notice are being served by hand with the clerk of the Federal Circuit at the following address:

U.S. Court of Appeals for the Federal Circuit
717 Madison Place, N.W.
Washington, DC 20439



Sean C. Flood
Reg. No. 64,378
Virginia State Bar No. 76,730

Robinson Intellectual Property Law Office, P.C.
3975 Fair Ridge Drive, Suite 20 North
Fairfax, Virginia 22033
(571) 434-6789



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
08/169,127	12/20/1993	HISATO SHINOHARA	0756-0945	2677
31780	7590	11/10/2010	EXAMINER	
Robinson Intellectual Property Law Office, P.C.			PADGETT, MARIANNE L	
3975 Fair Ridge Drive			ART UNIT	PAPER NUMBER
Suite 20 North				
Fairfax, VA 22033			1715	
			MAIL DATE	DELIVERY MODE
			11/10/2010	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE BOARD OF PATENT APPEALS
AND INTERFERENCES

Ex parte HISATO SHINOHARA and AKIRA SUGAWARA

Appeal 2009-015419
Application 08/169,127
Technology Center 1700

Before CHUNG K. PAK, TERRY J. OWENS, and JEFFREY T. SMITH,
Administrative Patent Judges.

OWENS, *Administrative Patent Judge*.

DECISION ON REQUEST FOR REHEARING

The Appellants request reconsideration of the affirmance of the rejection of claims 61-80, 91-94, 101, 104-107, 131 and 140-175 under the judicially created doctrine of obviousness-type double patenting over claims 1-30 of Shinohara in our Decision mailed July 29, 2010 (Request 1).

The Appellants argue that we improperly considered Shinohara's disclosed step of etching insulator 59 off of crystallized semiconductor islands 58 (col. 5, ll. 48-51, 59-61; col. 6, l. 11; Fig. 7B) (which corresponds to the Appellants' claim 61 recited step of "removing an insulating layer

comprising silicon oxide from an upper surface of the crystallized semiconductor layer") as being included in Shinohara's claim 1 step of "forming a plurality of thin film transistors using the crystallized semiconductor film as at least channel regions of the thin film transistors" (Request 6). The Appellants argue that if Shinohara's claim is interpreted in that manner, then the Appellants' "forming" step, which is described in a specification which Shinohara and the Appellants have in common, likewise should be interpreted as including the recited "removing" step, thereby rendering the recited "removing" step superfluous. *See id.*

"[D]uring examination proceedings, claims are given their broadest reasonable interpretation consistent with the specification." *In re Translogic Tech. Inc.*, 504 F.3d 1249, 1256 (Fed. Cir. 2007), quoting *In re Hyatt*, 211 F.3d 1367, 1372 (Fed. Cir. 2000). The broadest reasonable interpretation consistent with the specification of the Appellants' claim 61 limitation "forming a plurality of thin film transistors" does not include the step of "removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer" because that step is positively recited in the claim. The broadest reasonable interpretation of Shinohara's "forming a plurality of thin film transistors" claim limitation consistent with Shinohara's specification, which is the same as the Appellants' specification, includes the steps disclosed as part of forming the plurality of thin film transistors (col. 5, ll. 48-51, 59-61; col. 6, ll. 11-23; Fig. 7B) but not recited in Shinohara's claim 1, such as the step of etching off insulator layer 59 (which corresponds to the Appellants' claim 61 recited step of "removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer").

Thus, the difference between the Appellants' claim 61 and Shinohara's claim 1 is that the Appellants' claim 61 positively recites the "removing" step, whereas that step is subsumed in Shinohara's claim 1 term "forming a plurality of thin film transistors". If the Appellants' application issued as a patent without a terminal disclaimer, then it and the Shinohara patent could be sold to different parties, thereby subjecting someone who performs the Appellants' "removing an insulating layer" step, which is subsumed in Shinohara's "forming a plurality of thin film transistors" step, to lawsuits by different parties for the same infringing act. Preventing such multiple lawsuits for the same infringing act is one of the functions served by the doctrine of obviousness-type double patenting and terminal disclaimers. *See Form PTO/SB/26 (01-08), Terminal Disclaimer to Obviate a Double Patenting Rejection over a "Prior" Patent, in Manual of Patent Examining Procedure* § 1490 (8th ed., Rev. 7, July 2008) ("The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.")

The Appellants argue, in reliance upon *In re Vogel*, 422 F.2d 438 (CCPA 1970), that the use of a patent's specification in an obviousness-type double patenting rejection is limited to determining the meaning of a word in a claim (Request 2-4).

The Court in *Vogel* stated that "in certain instances it [a patent's disclosure] may be used as a dictionary to learn the meaning of terms in a claim." *Vogel*, 422 F.2d at 441. Although the terms in *Vogel* were

individual words, such as “meat” and “pork”, *see Vogel*, 422 F.2d at 442, *Vogel* does not limit the terms whose meanings are learned to individual words.¹ Also, the Court stated:

The disclosure, however, sets forth at least one tangible embodiment within the claim, and it is less difficult and more meaningful to judge whether that thing has been modified in an obvious manner. It must be noted that this use of the disclosure is not in contravention of the cases forbidding its use as prior art, nor is it applying the patent as a reference under 35 U.S.C. § 103, since only the disclosure of the invention claimed in the patent may be examined.

Vogel, 422 F.2d at 422. In accord with *Vogel*, in determining the meaning of Shinohara’s claim term “forming a plurality of thin film transistors” we relied only upon an examination of the disclosure of Shinohara’s claimed invention (Decision 8).

The Appellants argue that it was improper for the Board to interpret Shinohara as disclosing a silicon oxide insulator 59 when Shinohara discloses that insulator 59 can be silicon oxide or silicon nitride (col. 5, ll. 46-47) (Request 5-6).

Shinohara’s disclosure that insulator 59 can be silicon nitride does not take away the disclosure of a silicon oxide insulator 59.

Regarding other groups of the Appellants’ claims, i.e., claims 140, 153 and 164, claims 71, 76, 164 and 165 and claims 141 and 154, the Appellants provide arguments similar to those set forth with respect to claim 61 that we improperly used Shinohara’s specification in construing Shinohara’s claims (Request 6-9).

¹ The relevant meaning of “term” is “a word or expression that has a precise meaning in some uses or is peculiar to a science, art, profession, or subject <legal ~ s>”. *Webster’s New Collegiate Dictionary* 1203 (G. & C. Merriam 1973). Thus, “term” is not limited to individual words.

Appeal 2009-015419
Application 08/169,127

Those groups of claims were addressed in our Decision (Decision 9-10). Our use of Shinohara's specification in interpreting Shinohara's claims was proper for the reasons set forth above with respect to the Appellants' claim 61.²

DECISION/ORDER

We have considered the Appellants' Request for Rehearing, but for the above reasons we decline to make any change to our Decision.

No time period for taking any subsequent action in connection with this appeal may be extended under 37 C.F.R. § 1.136(a).

DENIED

ssl

ROBINSON INTELLECTUAL PROPERTY LAW OFFICE, P.C.
3975 FAIR RIDGE DRIVE
SUITE 20 NORTH
FAIRFAX, VA 22033

² The Appellants argue that there is no limitation in Shinohara's claims corresponding to the Appellants' peripheral circuit in claims 71, 76, 164 and 165 (Request 7-8). One of ordinary skill in the art would have interpreted Shinohara's claim term "driving circuit" in claim 61 as including a peripheral circuit, i.e., a circuit in a peripheral area (other semiconductor islands on the same substrate) in view of Shinohara's disclosure that other semiconductor islands on the same substrate can include a peripheral circuit for driving the pixel TFTs (col. 5, ll. 55-58),



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
08/169,127	12/20/1993	HISATO SHINOHARA	0756-0945	2677
31780	7590	07/29/2010		
Robinson Intellectual Property Law Office, P.C.			EXAMINER	
3975 Fair Ridge Drive			PADGETT, MARIANNE L	
Suite 20 North				
Fairfax, VA 22033			ART UNIT	PAPER NUMBER
			1715	
			MAIL DATE	DELIVERY MODE
			07/29/2010	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE BOARD OF PATENT APPEALS
AND INTERFERENCES

Ex parte HISATO SHINOHARA and AKIRA SUGAWARA

Appeal 2009-015419
Application 08/169,127
Technology Center 1700

Before CHUNG K. PAK, TERRY J. OWENS, and JEFFREY T. SMITH,
Administrative Patent Judges.

OWENS, *Administrative Patent Judge.*

DECISION ON APPEAL¹

STATEMENT OF THE CASE

The Appellants appeal under 35 U.S.C. § 134(a) from the Examiner's rejection of claims 61-80, 91-94, 101, 104-107, 131, and 140-180, which are

¹ The two-month time period for filing an appeal or commencing a civil action, as recited in 37 C.F.R. § 1.304, or for filing a request for rehearing, as recited in 37 C.F.R. § 41.52, begins to run from the "MAIL DATE" (paper delivery mode) or the "NOTIFICATION DATE" (electronic delivery mode) shown on the PTOL-90A cover letter attached to this decision.

¹ US 5,130,291 to Okuda issued July 14, 1992.

all of the pending claims. The sole rejection of claims 176-180 (under 35 U.S.C. § 112, first paragraph, written description requirement) is withdrawn in the Examiner's Answer (pp. 2-3). We have jurisdiction under 35 U.S.C. § 6(b).

The Invention

The Appellants claim methods for making an active matrix display device and a plurality of thin film transistors. Claim 61 is illustrative:

61. A method of manufacturing an active matrix display device having an active matrix circuit and a driving circuit, said method comprising:

forming an ion blocking film over a substrate;

forming a non-single crystalline semiconductor layer;

providing a first laser beam having a first cross section;

expanding said first cross section of the first pulsed laser beam along a first direction;

condensing the expanded laser beam along a second direction orthogonal to said first direction;

irradiating the non-single crystalline semiconductor layer with the condensed laser beam having a second cross section at a surface of the non-single crystalline semiconductor layer wherein a length of said second cross section along said first direction is longer than that of said first cross section and a width of said second cross section along said second direction is smaller than that of said first cross section;

moving a relative location of said substrate to the condensed laser beam along a third direction orthogonal to said first direction while irradiating the non-single crystalline semiconductor layer with the condensed laser beam whereby the non-single crystalline semiconductor layer is crystallized; removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer; and

forming a plurality of thin film transistors using the crystallized semiconductor layer as at least channel regions of the thin film transistors for the active matrix circuit and said driving circuit.

The Reference

Shinohara 6,261,856 B1 Jul. 17, 2001

The Rejections

The claims stand rejected as follows: claims 61-65, 71-75, 91, 144, 151, 155-163, 166, 167, and 173-175 under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which the Appellants regard as the invention; claims 61-65, 71-75, 91, 144, 151, 155-163, 166, 167, and 173-175 under 35 U.S.C. § 112, first paragraph, as failing to comply with the written description requirement; claims 61-65 and 71-75 under 35 U.S.C. § 112, first paragraph, as failing to comply with the enablement requirement; and claims 61-80, 91-94, 101, 104-107, 131, and 140-175 under the judicially created doctrine of obviousness-type double patenting over claims 1-30 of Shinohara.

OPINION

We reverse the rejections under 35 U.S.C. § 112, first paragraph, and affirm the obviousness-type double patenting rejection.

Rejection under 35 U.S.C. § 112, second paragraph

Issue

Have the Appellants indicated reversible error in the Examiner's determination that "removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer" would have been indefinite to one of ordinary skill in the art?

Analysis

“[T]he indefiniteness inquiry asks whether the claims ‘circumscribe a particular area with a reasonable degree of precision and particularity.’”

Marley Mouldings Ltd. v. Mikron Industries Inc., 417 F.3d 1356, 1359 (Fed. Cir. 2005), quoting *In re Moore*, 439 F.2d 1232, 1235 (CCPA 1971).

The Examiner argues that “removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer” is indefinite because “it is unclear from the claim language where this silicon oxide layer comes from, since it was never claimed to be deposited, hence it is unclear how one can remove something that does not necessarily exist” (Br. 4). The Examiner argues that it is not clear whether the silicon oxide layer is formed before the non-single crystalline semiconductor layer is irradiated, is formed by oxidation during the irradiation, or is a masking layer. *See id.*

The Appellants point out that their Specification discloses that “the semiconductor layer is covered by an insulating layer 59 made of silicon oxide or silicon nitride” (Spec. 11, 3-6), and that “[a]fter the crystallization, the insulator 59 is etched off” (Spec. 12:1) (Reply Br. 6-7, 10). Regarding the formation of the silicon oxide layer the Appellants argue that “[t]he claim intentionally omits this feature, is intended to broadly claim the invention with respect to this feature, and is abundantly clear that the source of the silicon oxide is not a critical feature of the claimed invention.” *See id.*

The Examiner’s argument that “it is unclear how one can remove something that does not necessarily exist” (Ans. 4) is not well taken because the recitation in the Appellants’ claims that an insulating layer is removed indicates that the insulating layer exists prior to removal. The claims do not

recite how the silicon oxide layer is formed and, therefore, broadly encompass every manner of forming the silicon oxide layer. The breadth of the claims, however, does not render them indefinite. *See In re Gardner*, 427 F.2d 786, 788 (CCPA 1970) (“Breadth is not indefiniteness”). The Examiner has not established that even if the Appellants’ silicon oxide layer can be formed in any of the ways argued by the Examiner (Ans. 4), one of ordinary skill in the art would not have known what it means to remove such a layer.

Conclusion of Law

The Appellants have indicated reversible error in the Examiner’s determination that “removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer” would have been indefinite to one of ordinary skill in the art.

Rejections under 35 U.S.C. § 112, first paragraph

Issue

Have the Appellants indicated reversible error in the Examiner’s determination that the Appellants’ original disclosure fails to provide written descriptive and enabling support for “removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer”?

Analysis

The Examiner argues that the Appellants’ original disclosure “does not reasonably provide enablement for removal of ‘an insulating layer comprising silicon oxide’ from an unknown source in an uncertain relationship to the irradiation step itself” (Ans. 5).

The Appellants argue that “it would not require undue experimentation to understand that the present invention supports and enables removal of any insulating layer comprising silicon oxide from an upper surface of a crystallized semiconductor layer” (Reply Br. 9).

A specification complies with the 35 U.S.C. § 112, first paragraph, enablement requirement if it allows those of ordinary skill in the art to make and use the claimed invention without undue experimentation. *See In re Wright*, 999 F.2d 1557, 1561 (Fed. Cir. 1993); *Atlas Powder Co. v. E.I. du Pont De Nemours & Co.*, 750 F.2d 1569, 1576 (Fed. Cir. 1984). The Examiner has not established that even if the Appellants’ silicon oxide layer can be formed in any of the ways argued by the Examiner (Ans. 4), one of ordinary skill in the art would not have been able to carry out the claimed method without undue experimentation.

The Examiner argues that the Appellants’ claims are broader than the scope of the enabling disclosure and, therefore, lack adequate written descriptive support (Ans. 5).

The Appellants argue that “it is wholly inappropriate to extend the written description requirement to include all possible variations that fall within the scope of a given claim” (Reply Br. 6).

For an applicant to comply with the 35 U.S.C. § 112, first paragraph, written description requirement, the applicant’s specification must “convey with reasonable clarity to those skilled in the art that, as of the filing date sought, he or she was in possession of the invention.” *Carnegie Mellon University v. Hoffmann-La Roche Inc.*, 541 F.3d 1115, 1122 (Fed. Cir. 2008), quoting *Vas-Cath Inc. v. Mahurkar*, 935 F.2d 1555, 1563-64 (Fed. Cir. 1991). The Appellants’ original disclosure indicates that the Appellants

were in possession of a method wherein a silicon oxide layer is removed from an upper surface of a crystallized semiconductor layer, i.e., “[a]fter the crystallization, the insulator 59 is etched off” (Spec. 12:1). The Examiner has not established that even if the Appellants’ silicon oxide layer can be formed in any of the ways argued by the Examiner (Ans. 4), the Appellants’ original disclosure would have failed to indicate possession of a method wherein the silicon oxide layer is removed.

Conclusion of Law

The Appellants have indicated reversible error in the Examiner’s determination that the Appellants’ original disclosure fails to provide written descriptive and enabling support for “removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer”.

Obviousness-type double patenting rejection

Issue

Have the Appellants indicated reversible error in the Examiner’s determination that under the doctrine of obviousness-type double patenting the Appellants’ argued claim limitations do not distinguish the Appellants’ claims over those of Shinohara?

Analysis

The Appellants argue that the step of “removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer” distinguishes the Appellants’ claims 61, 71, and 151 over the claims of Shinohara (Br. 22).

In an obviousness-type double patenting determination the patent’s disclosure, in certain instances, “may be used as a dictionary to learn the

meaning of terms in a claim.” *See In re Vogel*, 422 F.2d 438, 441 (CCPA 1970).

Shinohara’s claim 1 recites “forming a plurality of thin film transistors using the crystallized semiconductor film as at least channel regions of the thin film transistors.” Shinohara’s Specification indicates that “forming a plurality of thin film transistors” means removing a silicon oxide layer (59) from a plurality of crystallized semiconductor islands (59), forming a gate insulating layer (53), forming a gate electrode (56) on the gate insulating layer (53) over each island (59), and doping each island (59) using the gate electrode (56) as a mask so as to form source and drain regions (57) in each semiconductor island (58) (col. 6, ll. 11-23; Fig. 7D). Hence, one of ordinary skill in the art would have interpreted Shinohara’s claim 1 term “forming a plurality of thin film transistors” as including the step of “removing an insulating layer comprising silicon oxide from an upper surface of the crystallized semiconductor layer” in the Appellants’ claims 61, 71, and 151. Because Shinohara’s claim 1 and the Appellants’ claims 61, 71, and 151 differ in scope, the proper double patenting rejection is obviousness-type double patenting. *See Vogel*, 422 F.2d at 441 (“By ‘same invention’ we mean identical subject matter. Thus the invention defined by a claim reciting ‘halogen’ is not the *same* as that defined by a claim reciting ‘chlorine,’ because the former is broader than the latter”).

The Appellants argue that the step of “moving a relative location of said substrate to the condensed laser beam along a third direction orthogonal to said first direction” distinguishes the Appellants’ claims 66, 76, and 152 over the claims of Shinohara (Br. 22).

Shinohara's claim 1 recites "moving said substrate with respect to said laser beam in a direction [which corresponds to the Appellants' third direction] perpendicular [i.e., orthogonal] to said first direction".

The Appellants argue that the step of "forming a gate insulating film on said plurality of semiconductor islands wherein said gate insulating film covers a surface of the ion blocking film, said surface being exposed between the plurality of semiconductor islands" distinguishes the Appellants' claims 140, 153, and 164 over the claims of Shinohara (Br. 22-23).

Shinohara's claim 27 recites "forming a plurality of semiconductor islands on a blocking film" and "forming a gate insulating film on said plurality of semiconductor islands". For written descriptive support for the Appellants' claim limitation "said surface being exposed between the plurality of semiconductor islands" the Appellants rely upon their Figure 7C (Br. 8-9; fn 39) which shows that between the semiconductor islands (58, Fig. 7B) the ion blocking film (51, Fig. 7A) is covered by the gate insulating film (53). In view of Shinohara's Figure 7C one of ordinary skill in the art would have interpreted Shinohara's claim 27 term "forming a gate insulating film on said plurality of semiconductor islands" as meaning that the gate insulating film is formed over the semiconductor islands (58) and the blocking film (51) surface between the semiconductor islands (58).

The Appellants argue that the step of "forming a plurality of [thin film transistors, claims 71 and 76; semiconductor islands, claims 164 and 165] . . . for the active matrix circuit and said peripheral circuit" distinguishes the Appellants' claims 71, 76, 164, and 165 over the claims of Shinohara (Br. 22-24).

Shinohara's claim 1 recites "forming a plurality of thin film transistors using the crystallized semiconductor film as at least channel regions of the thin film transistors, whereby both of said active matrix circuit and said driving circuit are constituted with said thin film transistors." As indicated by Shinohara's Figure 7C the plurality of thin film transistors formed using the crystallized semiconductor film as the channel regions include semiconductor islands (58, Fig. 7B). Shinohara's disclosure that "[i]t is also possible to form other semiconductor islands on the same substrate in order to form a driver circuit or peripheral circuit for driving the pixel TFTs" (col. 5, ll. 55-58) indicates that the driving circuit in Shinohara's claim 1 is, or at least would have been suggestive of, a peripheral circuit.

The Appellants argue that "wherein the condensed laser beam has a second cross section on the substrate wherein a length of said second cross section along said first direction is longer than that of said first cross section and a width of said second cross section along said second direction is smaller than that of said first cross section" distinguishes the Appellants' claims 141 and 154 over the claims of Shinohara (Br. 22-24).

The steps in Shinohara's claim 1 of "expanding said first pulsed excimer laser beam only in a first direction, condensing the expanded pulsed excimer laser beam only in a second direction perpendicular to said first direction" cause the laser beam to have a second cross section along the first direction that is longer than that of a first cross section and a width of the second cross section along a second direction that is smaller than that of the first cross section, as indicated by a comparison of Figures 2A-2D of Shinohara and the Appellants.

As stated by the Federal Circuit in *Eli Lilly and Co. v. Barr Laboratories, Inc.*, 251 F.3d 955, 967-68 (Fed. Cir. 2001), “[t]he fundamental reason for the rule [of obviousness-type double patenting] is to prevent unjustified timewise extension of the right to exclude granted by a patent no matter how the extension is brought about” (quoting *In re Van Ornum*, 686 F.2d 937, 943-44 (CCPA 1982)). The Appellants’ argued claim limitations which are within the meaning of Shinohara’s claim terms would provide such an unjustified timewise extension of the Shinohara patent.

Conclusion of Law

The Appellants have not indicated reversible error in the Examiner’s determination that under the doctrine of obviousness-type double patenting the Appellants’ argued claim limitations do not distinguish the Appellants’ claims over those of Shinohara.

DECISION/ORDER

The rejections of claims 61-65, 71-75, 91, 144, 151, 155-163, 166, 167, and 173-175 under 35 U.S.C. § 112, second paragraph, claims 61-65, 71-75, 91, 144, 151, 155-163, 166, 167, and 173-175 under 35 U.S.C. § 112, first paragraph, written description requirement, and claims 61-65 and 71-75 under 35 U.S.C. § 112, first paragraph, enablement requirement are reversed. The rejection of claims 61-80, 91-94, 101, 104-107, 131, and 140-175 under the judicially created doctrine of obviousness-type double patenting over claims 1-30 of Shinohara is affirmed.

It is ordered that the Examiner’s decision is affirmed.

No time period for taking any subsequent action in connection with this appeal may be extended under 37 C.F.R. § 1.136(a).

AFFIRMED

Appeal 2009-015419
Application 08/169,127

PL
sld

Robinson Intellectual Property Law Office, P.C.
3975 Fair Ridge Drive
Suite 20 North
Fairfax VA 22033